



EXPEDITED PROCEDURE – EXAMINING GROUP 2831

S/N 09/537274

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Larry E. Mosley

Examiner: Eric Thomas

Serial No.: 09/537274

Group Art Unit: 2831

Filed: March 29, 2000

Docket No.: 884.240US1

Title: MULTI-LAYER CHIP CAPACITOR

Assignee: Intel Corporation

#18 / F
D. ET AL
6-26-03

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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TECHNOLOGY CENTER 2800

In response to the final Office Action mailed April 16, 2003, please amend the Application as noted below, and consider the appended remarks. In addition, please note that this Response has been filed in conjunction with a Request for Continued Examination, as well as an Information Disclosure Statement.